

## EAST Search History

## EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	425	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 10:56
S2	64	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or readin or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 10:58
S3	67	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 10:58

S4	67	(chip or (micro near3 chip) or (smart near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 11:13
S5	99	(chip or (micro near3 chip) or (smart near3 card)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 11:14
S6	18	(chip or (micro near3 chip) or (smart near3 card)) same (embed\$3) same (device or appliance or (circuit near3 board)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 11:22

S7	789	(chip or (micro near3 chip) or (smart near3 card)) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 11:25
S8	3121	713/193	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:15
S9	576	711/101	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:15
S10	3787	711/103	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:15
S11	1496	711/104	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:15
S12	28	S1 and S8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:16
S13	1	S1 and S9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:16
S14	11	S1 and S10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:16

S15	1	S1 and S11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:16
S16	53	S7 and S8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:56
S17	2	S7 and S9	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:56
S18	3	S7 and S10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:56
S19	0	S7 and S11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/29 13:56
S24	32	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same (protection or security or secure) same ((authorization or authentication) near3 data)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/04/30 07:41
S25	108	(chip or (micro near3 chip) or (smart near3 card)) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing)) and (software and hardware) same (protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 10:40

S26	2	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) and (password or (protection near3 data)) same (allow or deny) same (access) and (control same (bit\$2 o signal\$2)) same (software or hardware) same (protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:03
S27	40	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) and (password or (protection near3 data)) same (allow or deny) same (access) and (control same (bit\$2 or signal\$2)) same (software or hardware) same (protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:04
S28	49	(password or (protection near3 data)) same (allow or deny) same (access) and (control same (bit\$2 or signal\$2)) same (software and hardware) same (protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:08
S29	1699	711/145	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:20
S31	30	S29 and "s7"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:21

S32	828	(chip or (micro near3 chip) or (smart near3 card)) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:21
S33	3	S29 and S32	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:21
S34	447	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:23
S35	2	S29 and S34	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 11:23
S36	10	("20020101763"   "20030126513"   "5794033"   "5930826"   "6031757").PN. OR ("6731536").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2008/08/25 14:01
S37	1392	(chip or (micro near3 chip) or microprocessor or processor) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2 or signal\$2) same (hardware and software))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 14:58
S38	3372	713/193	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 14:59

S39	3307	711/163	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 14:59
S40	1149	711/164	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 14:59
S41	1699	711/145	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 14:59
S43	39	S37 and S38	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 15:00
S44	42	S37 and S39	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 15:00
S45	21	S37 and S40	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 15:00
S46	5	S37 and S41	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2008/08/25 15:00
S47	94	(chip or (micro near3 chip) or microprocessor or processor) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2 or signal\$2) same (hardware and software) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 15:18
S49	685	(hardware same chip same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 15:57

S50	192	(chip or (micro near3 chip) or microprocessor or processor) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2 or signal\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 16:00
S51	108	(chip or (micro near3 chip)) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2 or signal\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 16:03
S52	29	(chip or (micro near3 chip)) same (memory or register) same (password or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2 or signal\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 16:04
S53	120	(chip or (micro near3 chip) or microprocessor or processor) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/25 16:34

S54	28	(chip or (micro near3 chip) or (smart near3 card)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/26 08:04
S55	1089951	(chip or (micro near3 chip) or (smart near3 card)) or (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/26 08:08
S56	1058705	(chip or (micro near3 chip)) or (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/26 08:10

S57	1058670	(chip or (micro near3 chip)) or (microprocessor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2008/08/26 08:10
S59	12	("5628023"   "6260131"   "6304944"   "6351797"   "6408373"   "6412043"   "6418523"   "6430664"   "6430670").PN. OR ("6643759").URPN.	US-PGPUB; USPAT; USOCR	AND	ON	2008/08/26 08:12
S61	1	"10536732"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/06/10 09:09
S62	2	("2003/0014653").URPN.	USPAT	AND	ON	2009/06/10 09:24
S63	9	(chip or (micro near3 chip) or microprocessor or processor) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2 or signal\$2) same (hardware) same protection) and mips	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:14
S65	0	Mips near3 instrcution near3 set	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:16
S66	535	Mips near3 instruction near3 set	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:16

S67	75	(Mips near3 instruction near3 set) same chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:16
S68	3	(Mips near3 instruction near3 set) near4 chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 11:17
S69	7	(Mips near3 instruction near3 set) near10 chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:36
S70	126	(chip or (micro near3 chip) or microprocessor or processor) same (memory or register) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:38
S71	75	(Mips near3 instruction near3 set) same chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:38
S73	995	(chip or (micro near3 chip) or (smart near3 card)) same ((protection or security or secure or authorization or authentication) near3 data) and ((authorizing or authenticating or denying) same (access or reading or writing))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:38
S74	1	S73 and S71	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:38

S75	525	(chip or (micro near3 chip)) same (microprocessor or processor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same ((protection or security or secure or authorization or authentication) near3 data)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S76	1	S75 and S71	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S77	1165449	(chip or (micro near3 chip)) or (microprocessor) same ((memory or NVRAM or (non near2 volatile near3 programmable near3 memory))) same (password or key or (protection near3 data)) and (allow\$3 or deny\$3 or authoriz\$3) same (access or read or write) same (memory or register) and (control same (bit\$2) same (hardware) same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S78	75	S77 and S71	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S79	824	(hardware same chip same protection)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S80	1	S79 and S71	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 12:39
S81	4169	713/193	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/07/30 13:57

S82	75	(Mips near3 instruction near3 set) same chip	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 13:57
S83	2	S82 and S81	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 13:57
S84	3787	711/163	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/07/30 13:57
S85	2	S82 and S84	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 13:57
S86	1332	711/164	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/07/30 13:57
S88	1829	711/145	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2009/07/30 13:57
S89	3	S82 and S88	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2009/07/30 13:57
S106	4972	((protect\$3 or control or security) near2 bit\$2) same (access) same (memory)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:56
S107	2781	((((protect\$3 or control or security) near2 bit\$2) same (access) same (memory)) and (chip or on- chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:56
S108	2828	((((protect\$3 or control or security) near2 bit\$2)) and ((secure or protected) with (memory or program)) and (chip or on-chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:57

S109	4723	713/193	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/17 21:58
S110	111	S108 and S109	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:58
S111	742	711/101	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/17 21:58
S112	3	S108 and S111	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:58
S113	5688	711/103	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/17 21:58
S114	132	S108 and S113	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:58
S115	1870	711/104	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/17 21:58
S116	26	S108 and S115	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:58
S117	2517	(((protection or control or security) near2 bit\$2)) and ((secure or protected) with (memory or program)) and (chip or on-chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:58
S118	17	(((protection or control or security) adj bit)) same ((secure or protected) with (memory or program)) same (chip or on-chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:59

S119	58	((protection or control or security) adj bit)) with ((secure or protected) with (memory or program)) and (chip or on-chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/17 21:59
S120	736	((protection or control or security) adj bit)) with (chip or on-chip)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/18 07:24
S121	4724	713/193	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/18 07:24
S122	5	S120 and S121	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/18 07:24
S123	742	711/101	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/18 07:24
S124	0	S120 and S123	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/18 07:24
S125	5707	711/103	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/18 07:24
S126	6	S120 and S125	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/18 07:24
S127	1873	711/104	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	AND	ON	2010/03/18 07:25
S128	8	S120 and S127	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/03/18 07:25
S129	6	((protection or control or security) adj bit)) with ((secure or protected) with (memory or program)) and (chip or on-chip) and stb	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/09/06 15:29

S130	18	((protection or control or security) adj bit)) with (chip or on-chip) and stb	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/09/06 15:30
S131	5	((protection or control or security) adj bit)) with ((secure or protected) with (memory or program)) and (chip or on-chip) and (conditional near3 access)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT	AND	ON	2010/09/06 16:51

**EAST Search History (Interference)**

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